REMARKS

Claims 1, 4, 6, 8, 10 and 17 are amended. Claim 13 has been cancelled. Claims 1, 3-12 and 15-22 are pending and under consideration.

The rejections under 35 U.S.C. § 103 are now discussed. Using independent claim 1 as an example, this claim recites an engine control unit and a main control unit integrated into a single chip with a processor to transmit a print start command to the engine control unit. The Examiner admits that Akiyama does not teach these features, but instead relies upon Gragg. However, Gragg teaches that print engine control and image sequencing control are performed by a single ASIC. Gragg, para. 27. Thus, there is no transmitting a start command from a main control unit to an engine control unit.

Furthermore, it would not have been obvious to combine the references, insofar as having a separate engine control unit is fundamental to the operation of Akiyama. Akiyama teaches a controller unit and an engine unit. The engine unit receives a designating command from the controller and in response transmits a condition change signal to the controller unit. Akiyama, Abstract. Thus, the essence of the reference is the communication between control unit and engine unit. Accordingly, combining these elements as taught by Gragg would have rendered Akiyama unable to operate. According to MPEP 2143.01, the modification proposed in the Office Action cannot render the prior art unsatisfactory for its intended purpose. *In re Gordon*, 221 USPQ 1125 (Fed. Cir. 1984).

Accordingly, withdrawal of the rejections is requested.

There being no further outstanding objections or rejections, it is submitted that the application is in condition for allowance. An early action to that effect is courteously solicited.

Finally, if there are any formal matters remaining after this response, the Examiner is requested to telephone the undersigned to attend to these matters.

REMARKS

Claims 1, 6, 8, 10 and 17 are amended. Claims 4 and 13 have been cancelled. Claims 1, 3, 5-12 and 15-22 are pending and under consideration.

The rejections under 35 U.S.C. § 103 are now discussed. Using independent claim 1 as an example, this claim recites a main control unit integrated into a single chip with a processor, and the video unit and the engine unit are driven by the processor. The Examiner admits that Akiyama does not teach these features, but instead relies upon Gragg. However, the image sequencing control of Gragg refers to images being displayed as a slide show (Gragg, para. 22). This reference also teaches the video-encoding, print engine control and image sequencing control are performed by a single ASIC. Thus, this reference does not teach either the single chip or the video and engine units being driven by the processor.

Furthermore, claim 1 recites the engine control unit includes a memory that stores state information of the engine mechanism. According to the current rejection of claim 4, the Examiner relies upon Kim. However, the memory 204 of Kim stores bit map data and address information to transmit to the printer engine CPU 202. Thus, information stored in the memory of the present invention is distinct from Kim.

Accordingly, withdrawal of the rejections is requested.

There being no further outstanding objections or rejections, it is submitted that the application is in condition for allowance. An early action to that effect is courteously solicited.

Finally, if there are any formal matters remaining after this response, the Examiner is requested to telephone the undersigned to attend to these matters.

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If there are any additional fees associated with filing of this Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

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